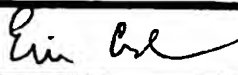


Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	NA- 10/771,630
				Filing Date	H rewith
				First Named Inventor	Gerard M. Col
				Group Art Unit	
				Examiner Name	
Sheet	2	of	2	Attorney Docket Number	CNTR.2095

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OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²
EC	CA	JIM HANDY, The Cache Memory Book, Second Edition, Figure 2.4	
EL	CB	INOUE, Performing Parallel Operations Subject to Operand Register Interference Using Operand History Storage, Pub. No.: US 2002/00499895 A1, April 25, 2002	
CC	CC	LIAPASTI et al, "Exceeding the Dataflow Limit via Value Prediction," Proceedings of the 29 <sup>th</sup> Annual IFEE/ACM International, <u>Symposium on Microarchitecture</u> , pp. 226-237.	

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Examiner Signature		Date Considered	4/2/06
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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